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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Stephen J. Barlow et al.

Examiner: Unknown

Serial No.: Unknown

Group Art Unit: Unknown

Filed: Herewith

Docket: 491.011US2

Title: DATA PROCESSING CIRCUITS AND INTERFACES



INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for review in connection with the above-identified patent application. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner be returned to the Applicants.

In accordance with 37 C.F.R. §1.98(d), copies of the listed documents are not provided as these references were previously cited by or submitted to the U.S. Patent Office in connection with Applicants' prior U.S. application, Serial No. 08/809,498, filed on March 24, 1997, which is relied upon for an earlier filing date under 35 U.S.C. §120.

Applicants respectfully request consideration of these references during prosecution of the above-identified matter. The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

STEPHEN J. BARLOW ET AL.

By their Representatives,

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By

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Date of Deposit: October 29, 2001

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to the Commissioner for Patents, Box Patent Application, Washington, D.C. 20231.

Form 1449*	Atty. Docket No.: 491.011US2	Serial No. Unknown
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Stephen J. Barlow et al.	
	Filing Date: Herewith	Group: Unknown

U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	3,611,309	10/05/1971	Zingg, R.J.	340	172.5	
	3,975,712	08/17/1976	Hepworth, E.C., et al.	340	147	
	4,342,082	07/27/1982	Brown, P.J., et al.	364	200	
	4,486,750	12/04/1984	Aoki, T.	340	825	
	4,785,393	11/15/1988	Chu, P.P., et al.	364	200	
	5,274,770	12/28/1993	Khim Yeoh, C.W., et al.	395	275	
	5,668,970	09/16/1997	Cowart, R., et al.	711	206	06/20/94
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JCS42 U.S. PTO
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**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
	0020185	12/10/1980	European	G06F	7/50	
	0055370	07/07/1982	European	G06F	3/04	
	0108664	05/16/1984	European	G06F	9/22	
	0124402	11/07/1984	European	G06F	9/26	
	0171190	02/12/1986	European	G06F	7/48	
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	0242003	10/21/1987	European	G06F	15/00	
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	1435406	05/12/1976	United Kingdom	G06F	13/00	
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OTHER DOCUMENTS

**Examiner Initial	(Including Author, Title, Date, Pertinent Pages, Etc.)
	"High-Speed Real-Time Event Processor", <u>IBM Technical Disclosure Bulletin</u> , Vol. 30, No. 2, IBM Corporation, New York, 632-634, (July 1987)
	"Transfer of Subsystem Control Blocks and Termination Status Blocks Between System and Subsystem by Registers", <u>IBM Technical Disclosure Bulletin</u> , Vol. 37, No. 9, IBM Corporation, New York, 147-148, (Sept. 1994)
	Clarke, P., "Mini MPU Aims at ASICs", <u>Electronic Engineering Times</u> , 24, (March 1991)

Examiner	Date Considered
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*Substitute Disclosure Statement Form (PTO-1449)

**EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.